

N .r No.: 14225-050001 . Je 1 of 6 Applicant(s): Kiyoshi Mita METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE →

FIG.1A

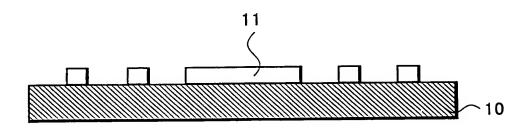
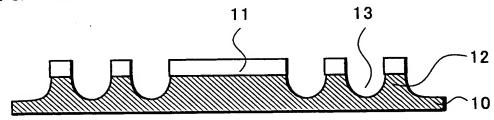


FIG.1B



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FIG.2

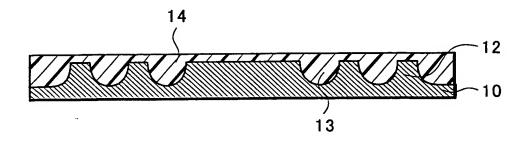


FIG.3

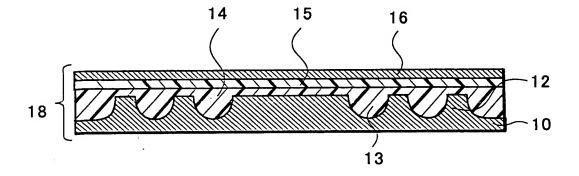


FIG.4

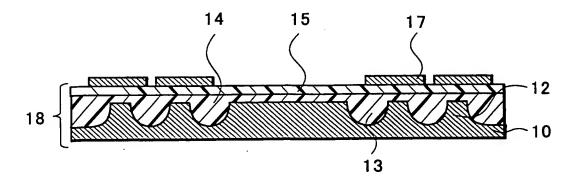


FIG.5

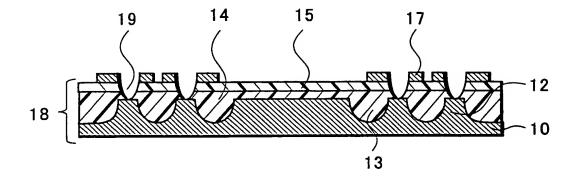


FIG.6

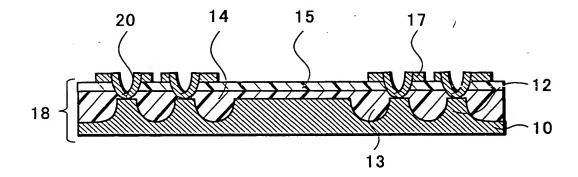


FIG.7

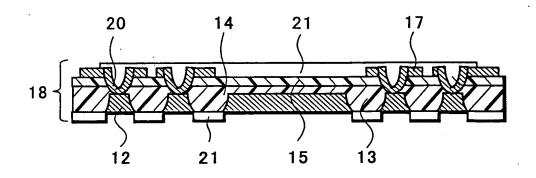


FIG.8

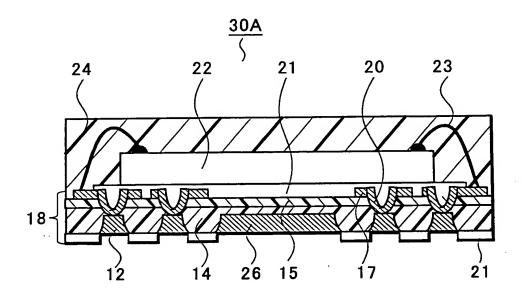
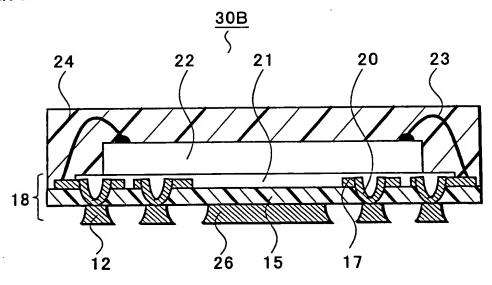


FIG.9



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FIG.10

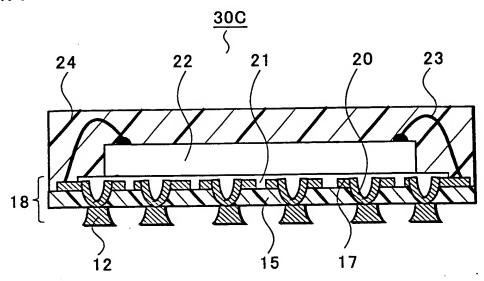


FIG.11

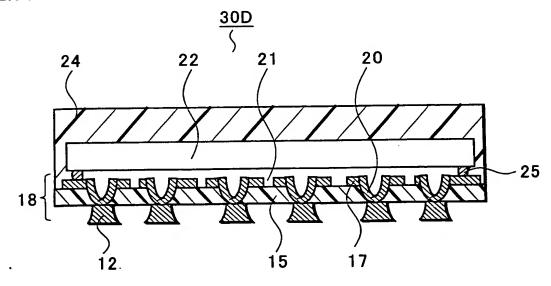


FIG.12A

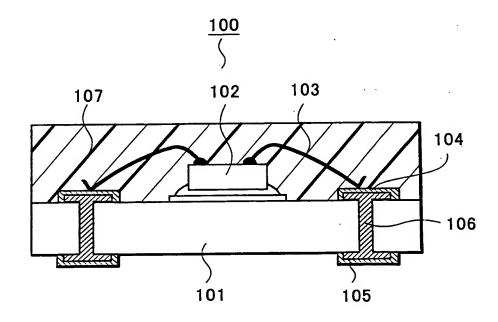


FIG.12B

